# This Page Is Inserted by IFW Operations and is not a part of the Official Record

## **BEST AVAILABLE IMAGES**

Defective images within this document are accurate representations of the original documents submitted by the applicant.

Defects in the images may include (but are not limited to):

- BLACK BORDERS
- TEXT CUT OFF AT TOP, BOTTOM OR SIDES
- FADED TEXT
- ILLEGIBLE TEXT
- SKEWED/SLANTED IMAGES
- COLORED PHOTOS
- BLACK OR VERY BLACK AND WHITE DARK PHOTOS
- GRAY SCALE DOCUMENTS

### IMAGES ARE BEST AVAILABLE COPY.

As rescanning documents will not correct images, please do not report the images to the Image Problem Mailbox.

#### IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

RECEIVED **CENTRAL FAX CENTER** 

Applicant: V. Vaganov

Group Art Unit:

FEB 0 5 2004

Serial No.: 09/596,837

Examiner: G. Goudreau

Attorney's Docket No.: SS-714-01

OFFICIAL

For:

METHOD FOR FABRICATING MICROSTRUCTURES WITH

DEEP ANISOTROPIC ETCHING OF THICK SILICON WAFERS

COMMISSIONER FOR PATENTS P.O. Box 1450 . Alexandria, VA 22313-1450

Filed: 06/19/2000

Date of this Paper:

2878

November 18, 2003

#### AMENDMENT

In response to the U.S. Patent and Trademark Office Action mailed 07/30/03, please amend the above-identified application as follows: